

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE (USPTO)

Serial Number	10/801503
Confirmation Number	1822
Filing Date	March 15, 2004
Title of Application	A FRACTIONAL-TYPE PHASE-LOCKED LOOP CIRCUIT WITH COMPENSATION OF PHASE ERRORS
First Named Inventor	Guido Gabriele ALBASINI
Assignee	STMicroelectronics, S.r.l.
Group Art Unit	2611
Examiner	Khanh C. Tran
Attorney Docket Number	2110-111-03
Nature of the Office Communication to which this is responding	Notice of Allowance
Date of the Office Communication	January 20, 2011
Nature of this Document	Comments on Statement of Reasons for Allowance

CERTIFICATE OF MAILING OR TRANSMISSION

I hereby certify that this correspondence is being transmitted via the Office electronic filing system, EFS-Web, addressed to: Mail Stop Issue Fee, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 15th day of April, 2011.

/Cindy McKee/

Signature

COMMISSIONER FOR PATENTS:

In response to the Notice of Allowability in this case mailed January 20, 2011, the Applicants, acting through their attorney, comment as follows. The Applicants thank the Examiner for allowing the claims. But the Applicants do not necessarily agree with the Examiner's Statement of Reasons for Allowance, and respectfully submit that the claims may be allowable for other reasons.

In the event additional fees are due as a result of this document, payment for those fees is enclosed. Should further payment be required to cover such fees you are hereby authorized to charge such payment to Deposit Account No. 07-1897.

If the Examiner believes that a phone interview would be helpful, he is respectfully requested to contact the Applicants' attorney, Kevin D. Jablonski, at (425) 455-5575.

Respectfully Submitted,

Graybeal, Jackson, LLP
Representatives for Applicant

/Kevin D. Jablonski/ Dated: April 15, 2011
Kevin D. Jablonski (kevin@graybeal.com)
Registration No. 50,401
USPTO Customer No.: 00996

Telephone: (425) 455-5575
Facsimile: (425) 455-1046